

(signature) 09/745033

Typ e	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Error
1	IS& R	L1	3314	((385/88) or (385/89) or (385/90) or (385/91) or (385/92) or (385/93) or (385/94)).CCLS.	USPAT; US-PGPU B; EPO; JPO	2004/ 08/17 13:53		0
2	BRS	L2	3193 74	(printed adj2 (circuit or circuits or board or boards or card or cards)) or (circuit adj2 (board or boards or card or cards or substrate or substrates)) or pcb or pcbs	USPAT; US-PGPU B; EPO; JPO	2004/ 08/17 13:53		0
3	BRS	L3	8617	2 with (perpendicular or perpendicularly or orthogonal or orthogonally)	USPAT; US-PGPU B; EPO; JPO	2004/ 08/17 13:53		0
4	BRS	L4	1596 30	(electromagnetic or em or emi or rf or rfi or interference) with (shield or shields or shielded or shielding or reduce or reduced or reduces or reducing or reduction or prevent or prevents or prevented or preventing or prevention)	USPAT; US-PGPU B; EPO; JPO	2004/ 08/17 13:54		0
5	BRS	L5	221	1 and 2 and 4	USPAT; US-PGPU B; EPO; JPO	2004/ 08/17 13:54		0
6	BRS	L6	88	1 and 3	USPAT; US-PGPU B; EPO; JPO	2004/ 08/17 13:54		0
7	BRS	L7	24	(5 or 6) and @pd>20040318	USPAT; US-PGPU B; EPO; JPO	2004/ 08/17 13:54		0